

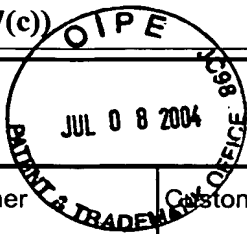
IFW

TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

(Under 37 CFR 1.97(b) or 1.97(c))

Docket No.
OKI.643

In Re Application Of: Kanji Otsuka et al.



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/775,223	02/11/2004	TBA	N/A	2631	9990

Title: SIGNAL TRANSMISSION APPARATUS AND INTERCONNECTION STRUCTURE

Address to:
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

37 CFR 1.97(b)

1. ☒ The Information Disclosure Statement submitted herewith is being filed within three months of the filing of a national application other than a continued prosecution application under 37 CFR 1.53(d); within three months of the date of entry of the national stage as set forth in 37 CFR 1.491 in an international application; before the mailing of a first Office Action on the merits, or before the mailing of a first Office Action after the filing of a request for continued examination under 37 CFR 1.114.

37 CFR 1.97(c)

2. ☐ The Information Disclosure Statement submitted herewith is being filed after the period specified in 37 CFR 1.97(b), provided that the Information Disclosure Statement is filed before the mailing date of a Final Action under 37 CFR 1.113, a Notice of Allowance under 37 CFR 1.311, or an Action that otherwise closes prosecution in the application, and is accompanied by one of:

☐ the statement specified in 37 CFR 1.97(e);

OR

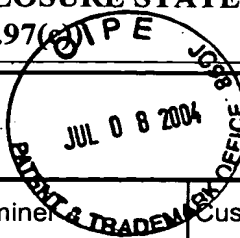
☐ the fee set forth in 37 CFR 1.17(p).

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SIGNAL TRANSMISSION APPARATUS AND INTERCONNECTION STRUCTURE**Payment of Fee**

(Only complete if Applicant elects to pay the fee set forth in 37 CFR 1.17(p))

- ☐ A check in the amount of _____ is attached.
- ☐ The Director is hereby authorized to charge and credit Deposit Account No. _____ as described below.
- ☐ Charge the amount of _____
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Dated: JULY 8, 2004

ADAM C. VOLENTINE
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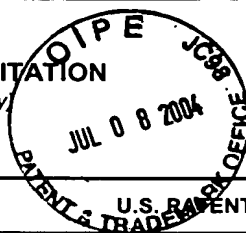
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TEL. NO. (703) 715-0870

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INFORMATION DISCLOSURE CITATION

(Use several sheets if necessary)



ATTY DOCKET NO. OKI.643	APPLICATION NO. 10/775,223
APPLICANT(S) Kanji OTSUKA et al.	
FILING DATE 02/11/2004	GROUP ART UNIT 2631

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

U.S. PATENT APPLICATION PUBLICATIONS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	A	11-284126	10/15/1999	JAPAN			✓	
	B	2000-174505	06/23/2000	JAPAN			✓	
	C	2002-124635	04/26/2002	JAPAN			✓	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

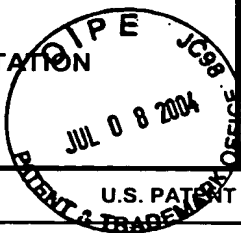
	D	Kanji Otsuka et al., "Measurement Potential Swing by Electric Filed on Package Transmission Lines," 2001 ICEP Proceedings, pages 490-495.
	E	Kanji Otsuka et al, "Measurement Evidence of Mirror Potential Traveling on Transmission Lines," The Fifth VLSI Packaging Workshop of Japan, 2000, pages 27 and 28.

EXAMINER	DATE CONSIDERED
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

INFORMATION DISCLOSURE CITATION

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ATTY DOCKET NO.

OKI.643

APPLICATION NO.

10/775,223

Kanji OTSUKA et al.

FILING

02/11/2004

GROUP ART

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	F	Kanji Otsuka et al., "Stacked Pair Line," JIEP Journal, Vol. 4, No. 7, pp. 556-561 (November 2001).
	G	Papers at the 15th conference of the Silicon Technology research group of the Japan Applied Physics society in a session Titled "Problems and prospects in ultra-high-speed multilayer interconnect technology," February 18, 2000, University of Tokyo, Yamagami Hall.

EXAMINER	DATE CONSIDERED
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